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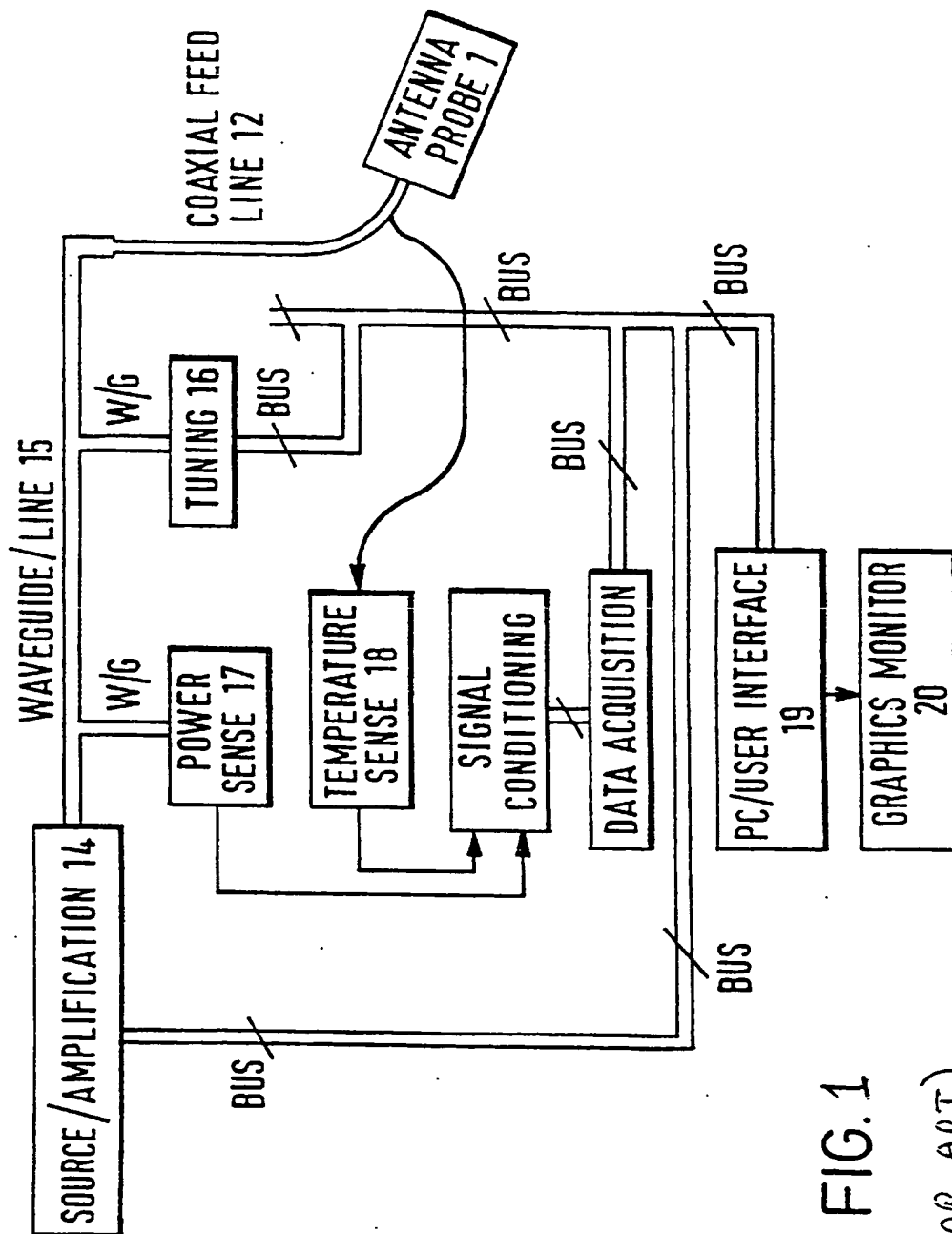


FIG. 1  
(PRIOR ART)

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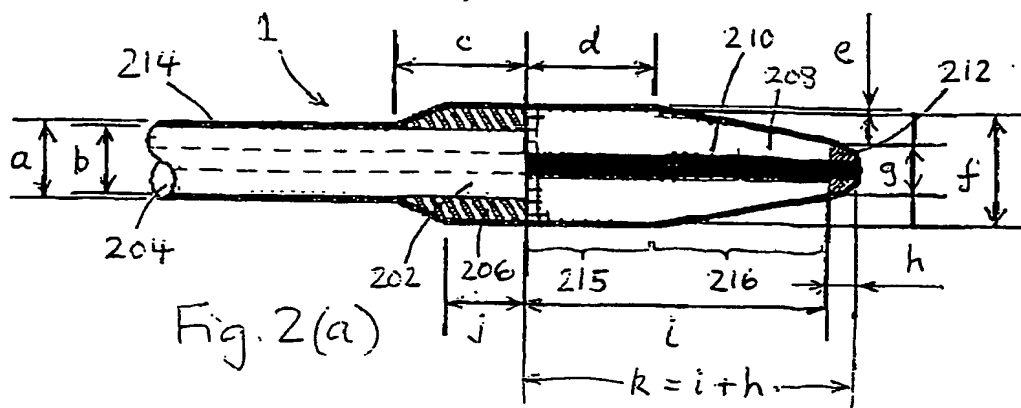


Fig. 2(a)

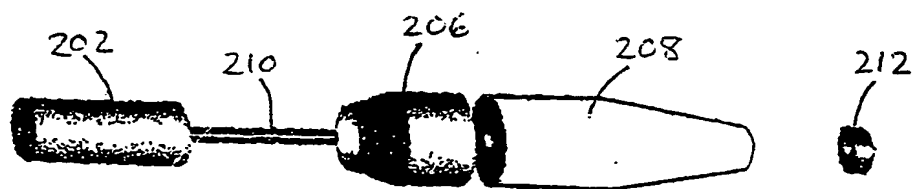


Fig. 2(b)

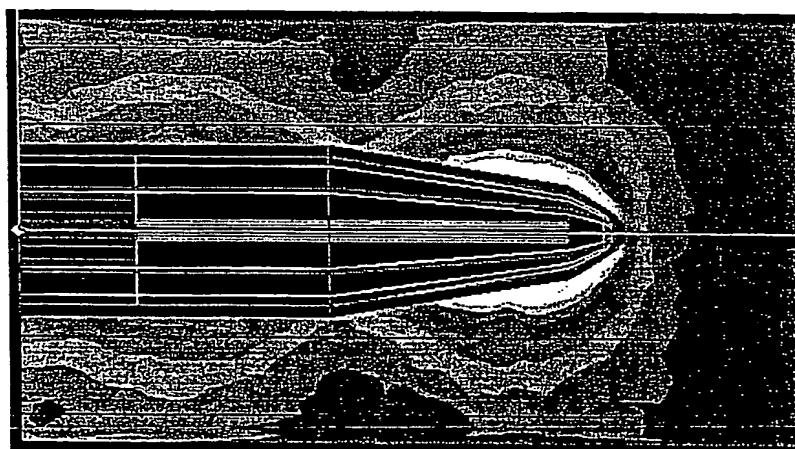


Fig. 2(c)

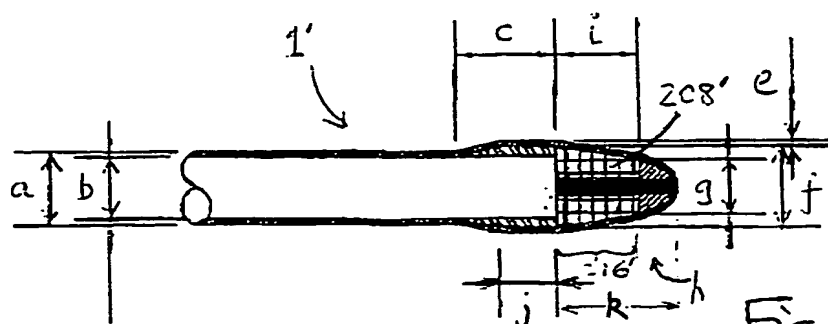


Fig. 3

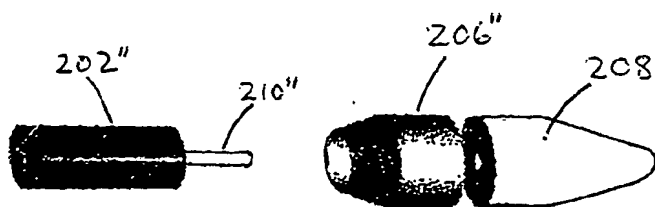
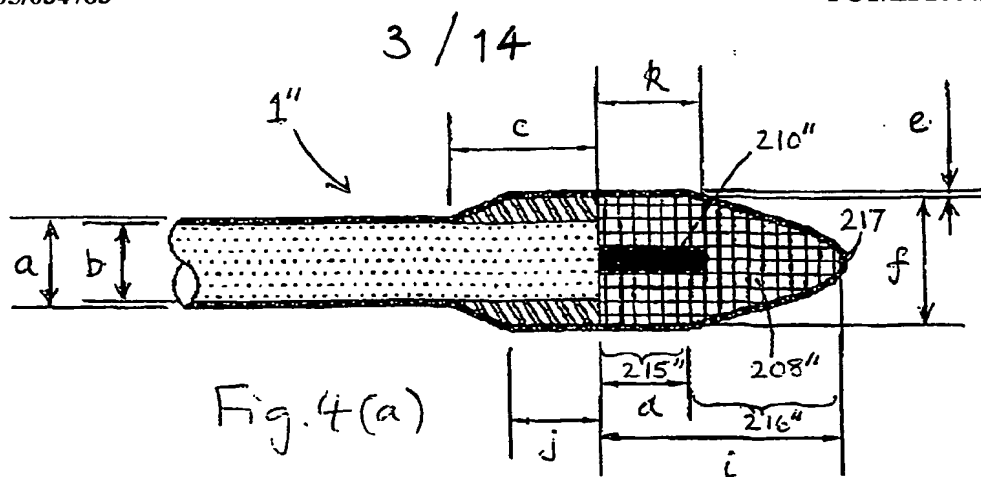


Fig. 4(b)

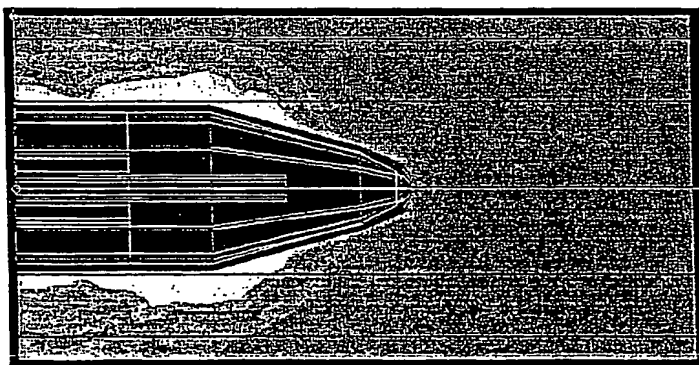
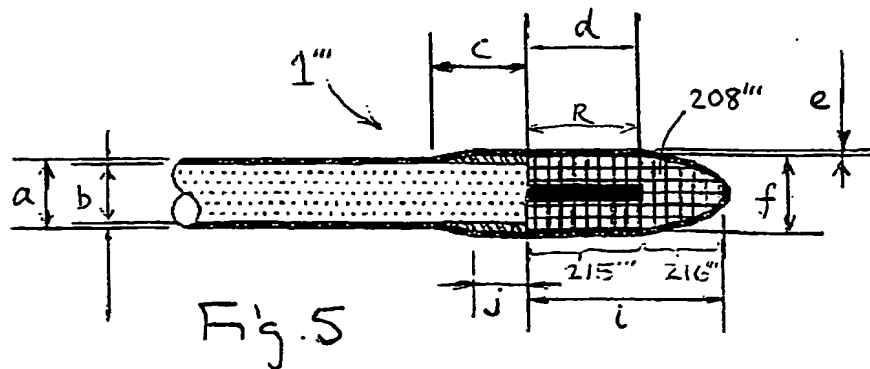


Fig. 4(c)



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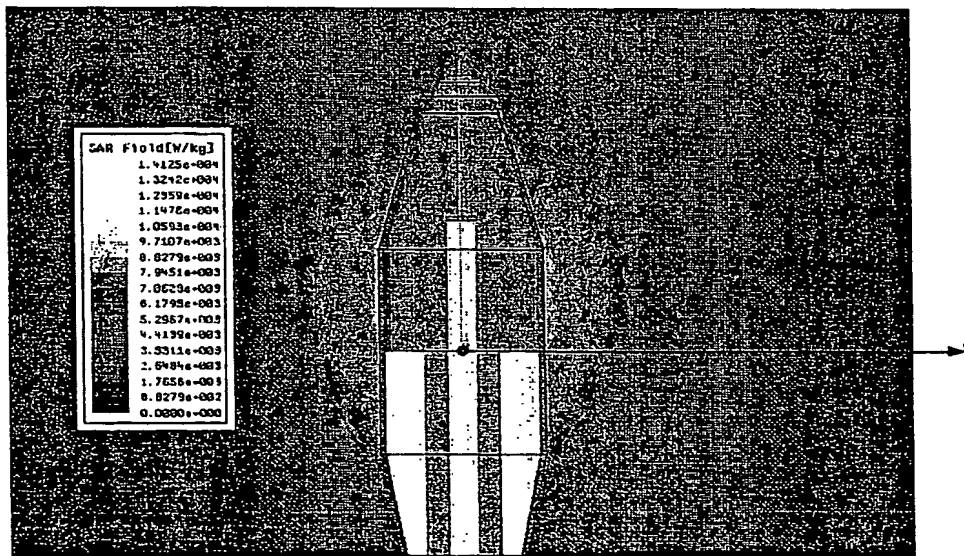
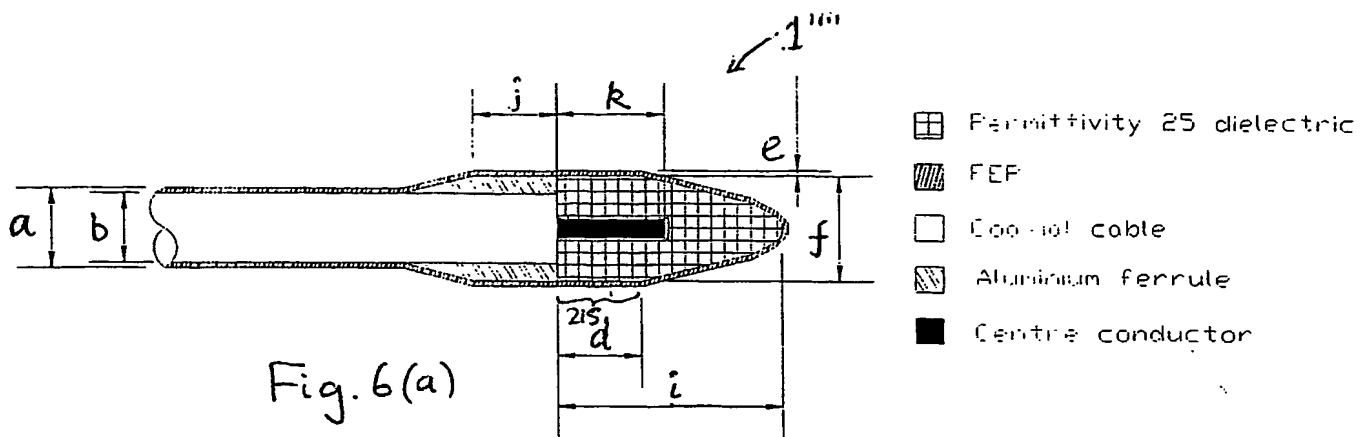


Fig. 6(b)

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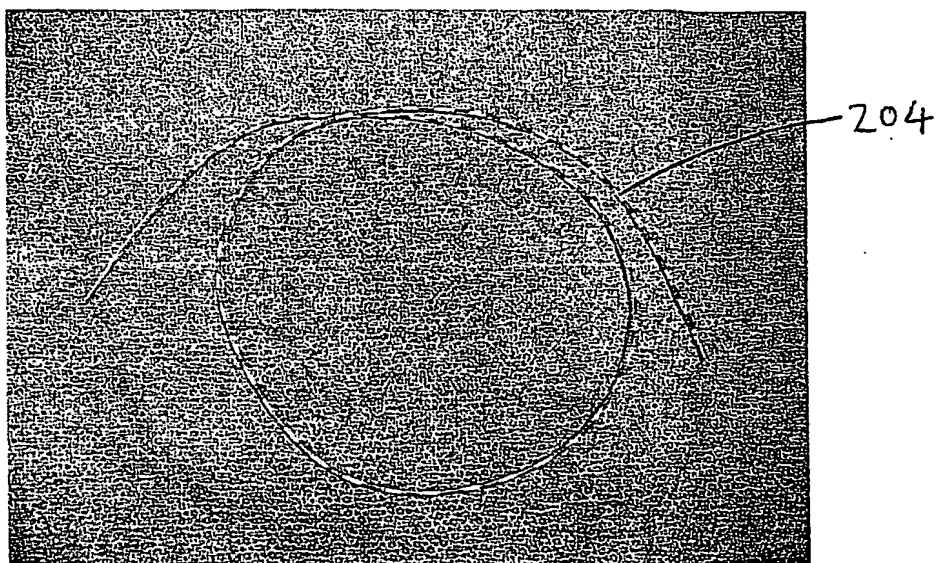


Fig. 7(a)

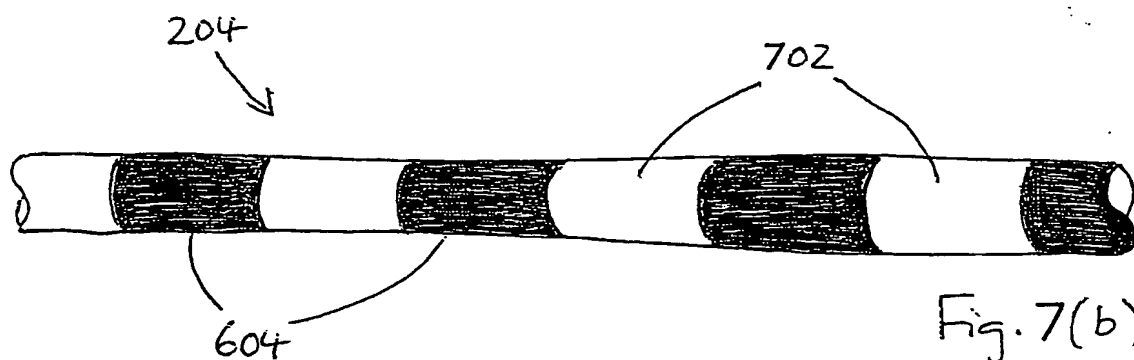


Fig. 7(b)

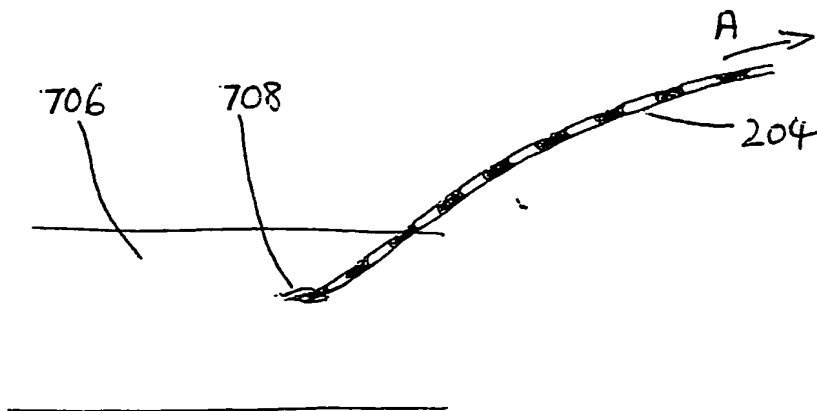


Fig. 7(c)

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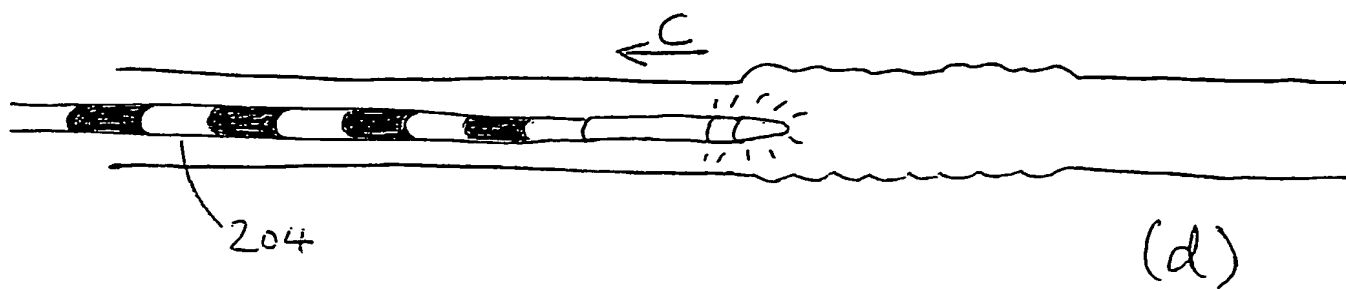
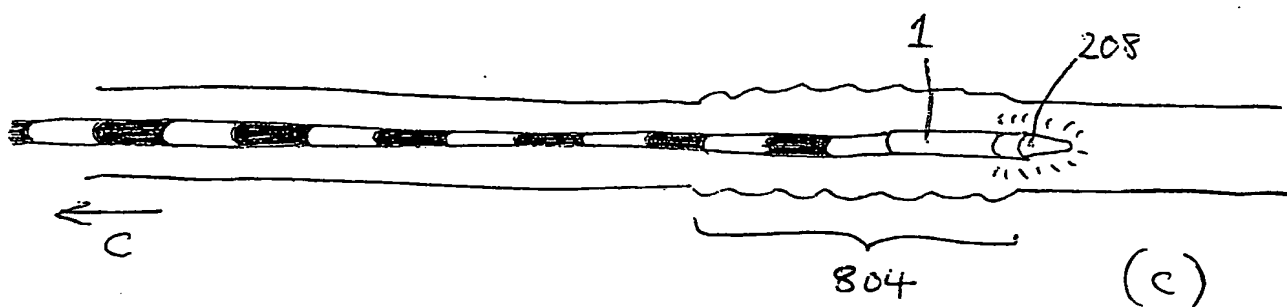
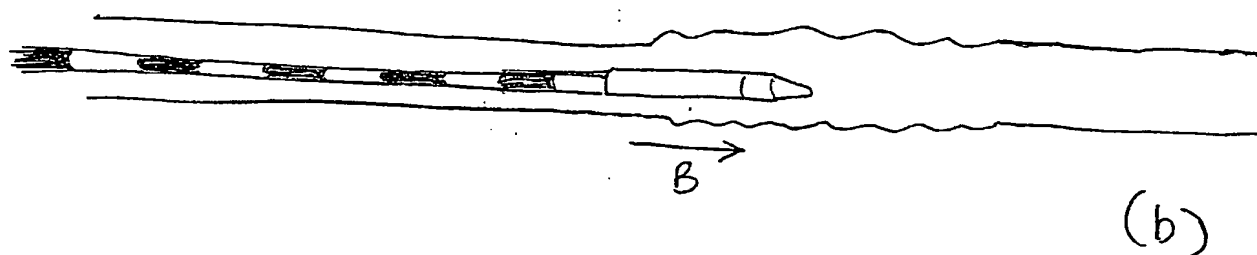
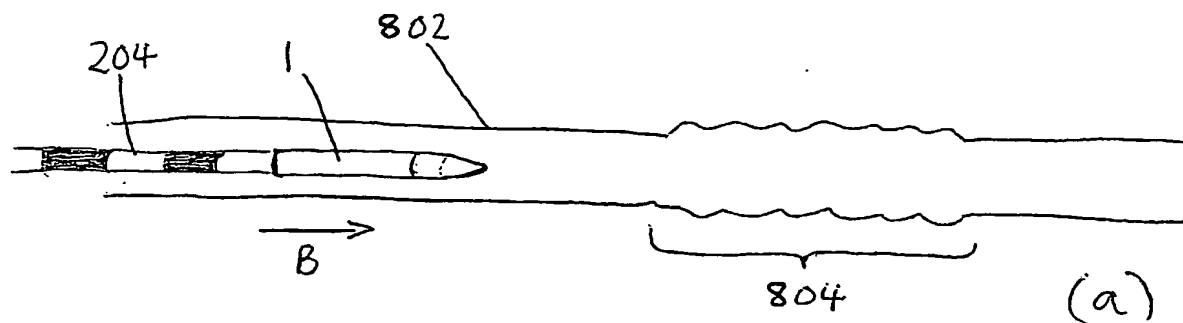


Fig. 8

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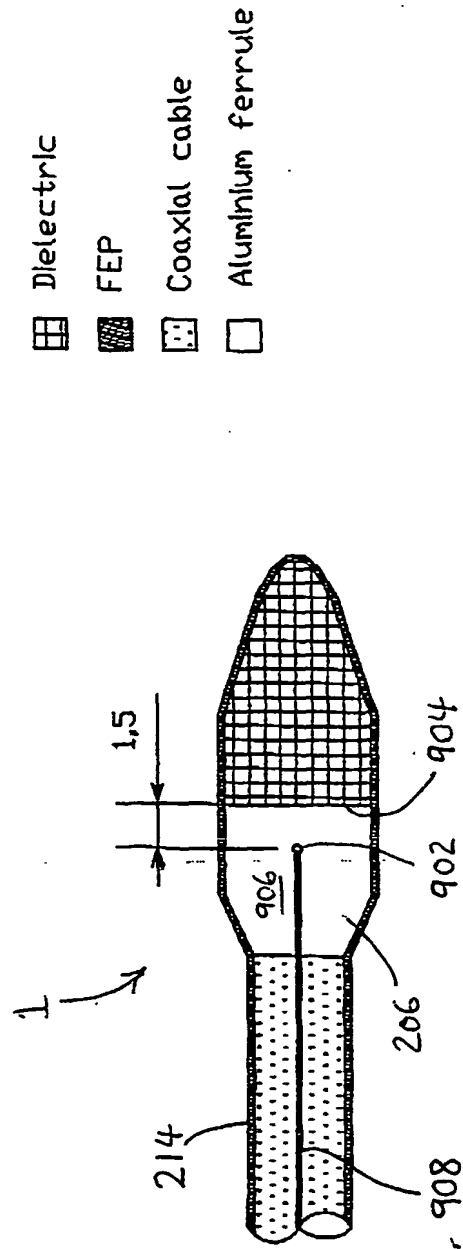


Fig. 9

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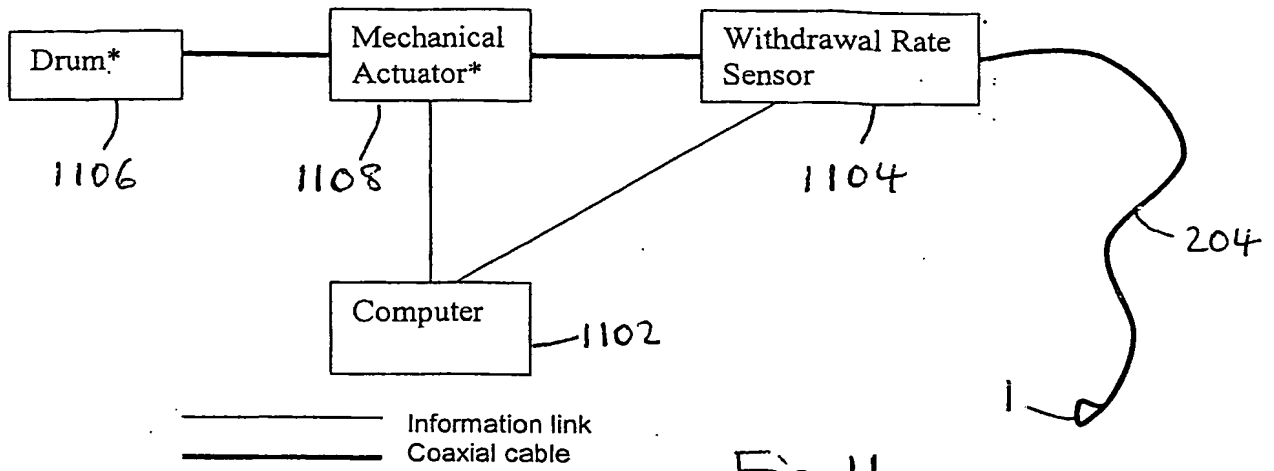


Fig. 11

Monitor Mains Power

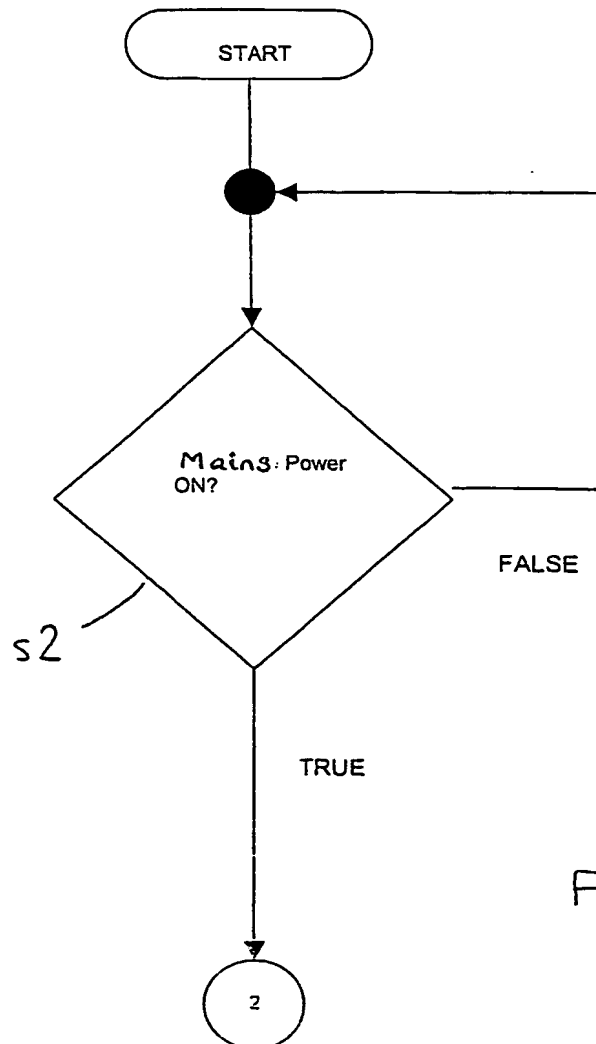


Fig. 10(a)



Monitor Treatment

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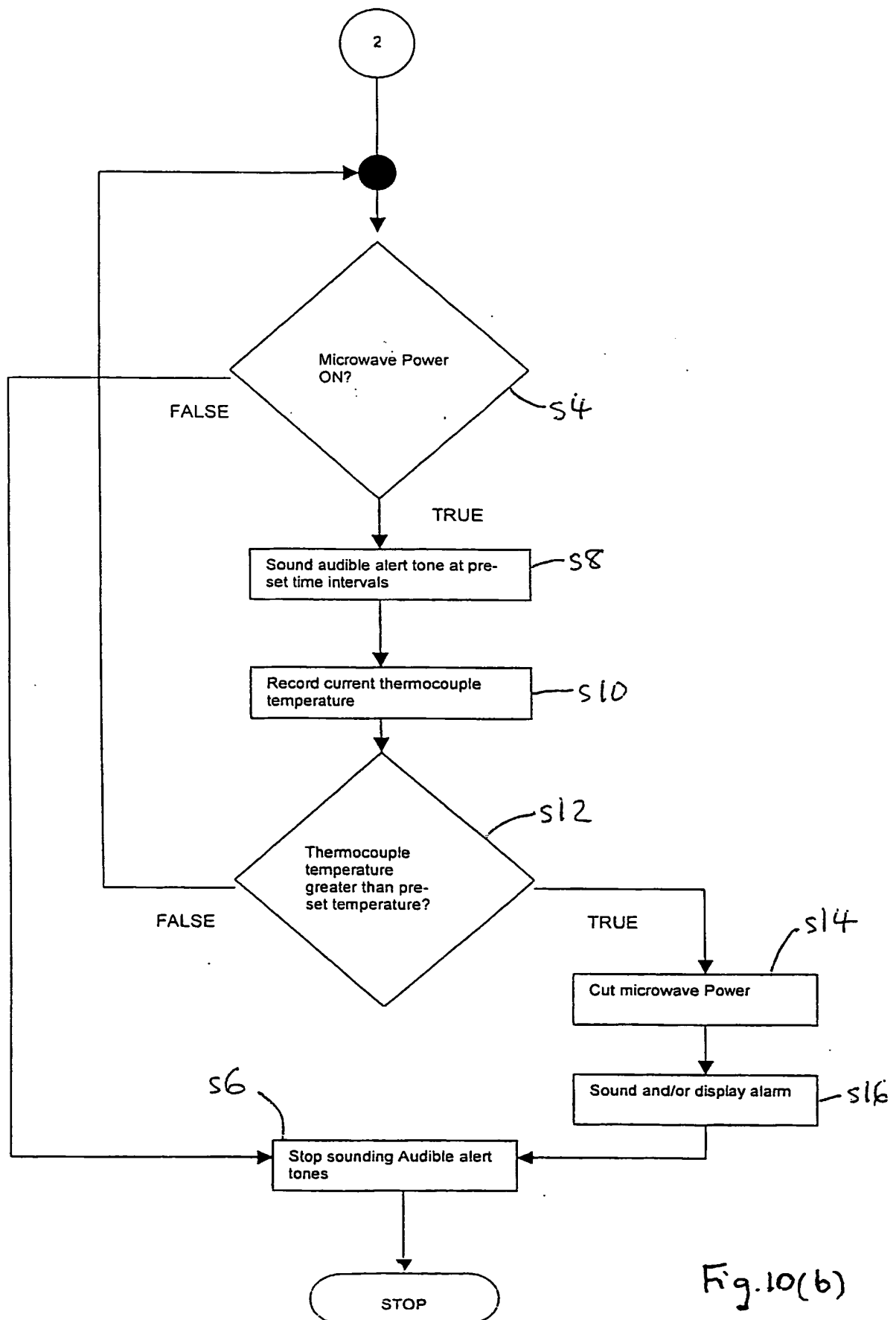
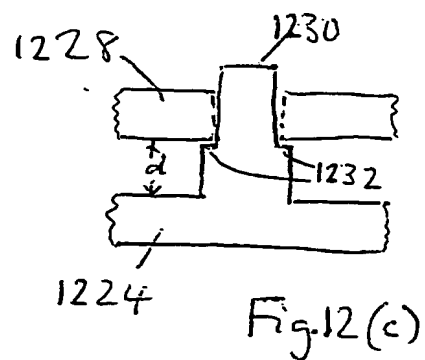
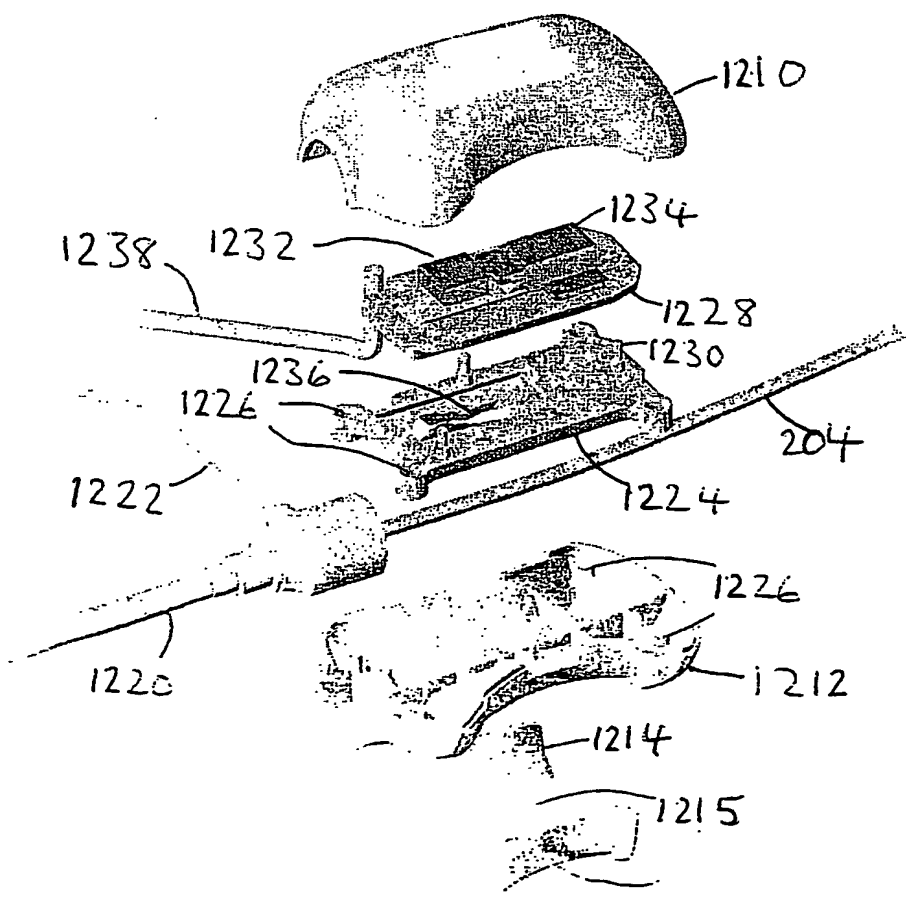
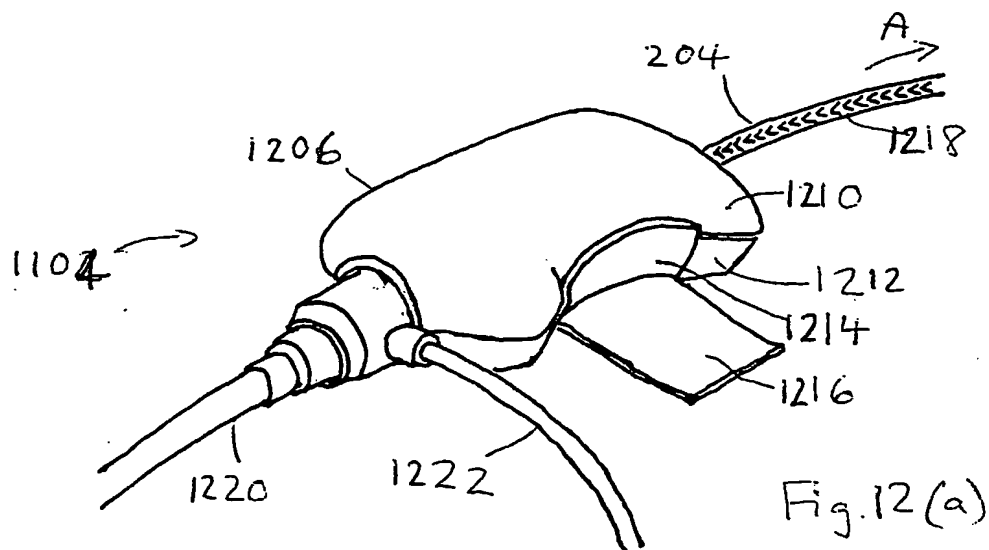


Fig. 10(b)

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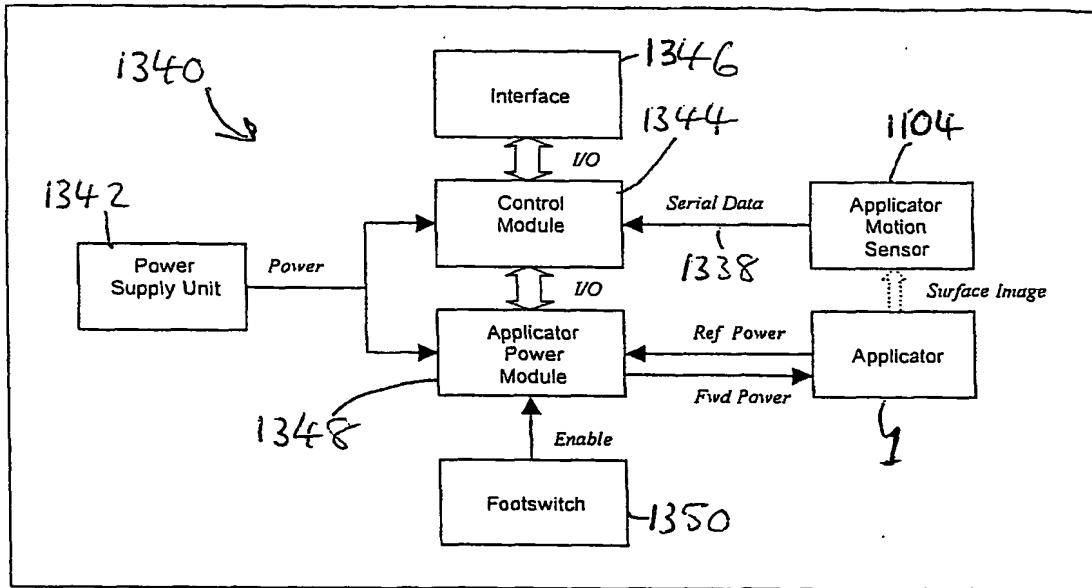


Fig.13

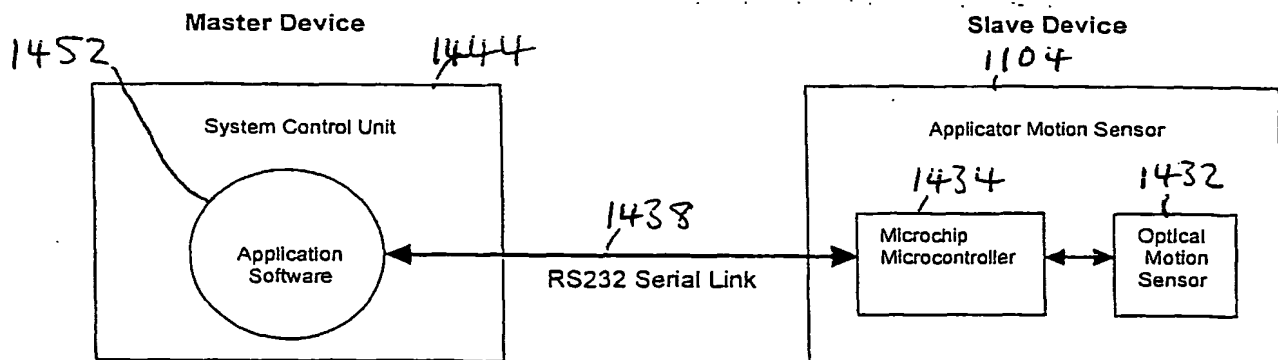


Fig.14

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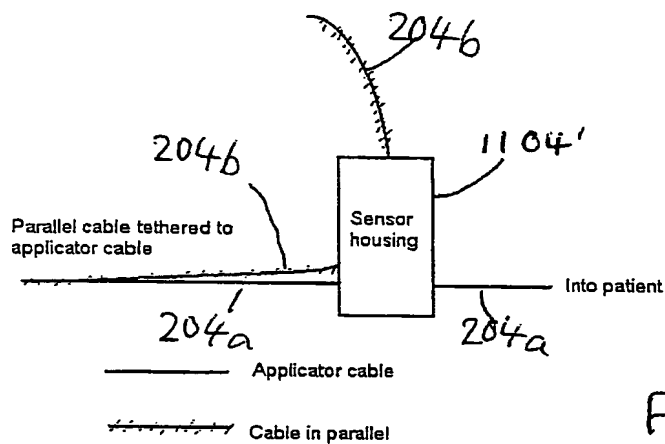


Fig.16

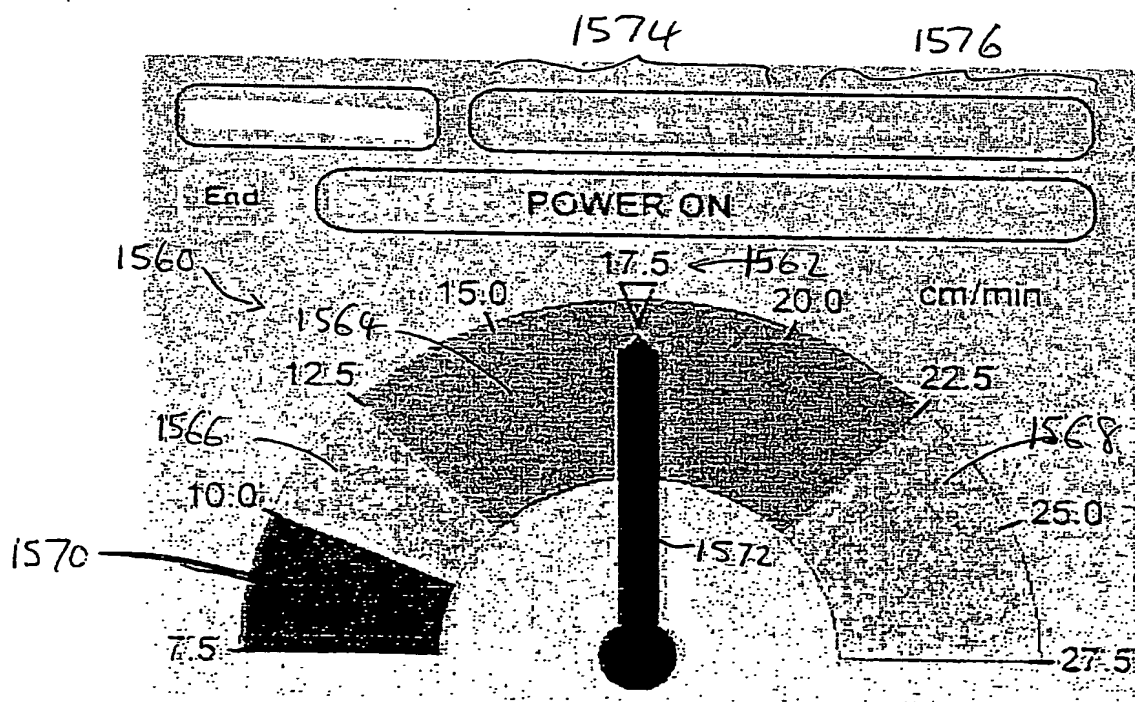
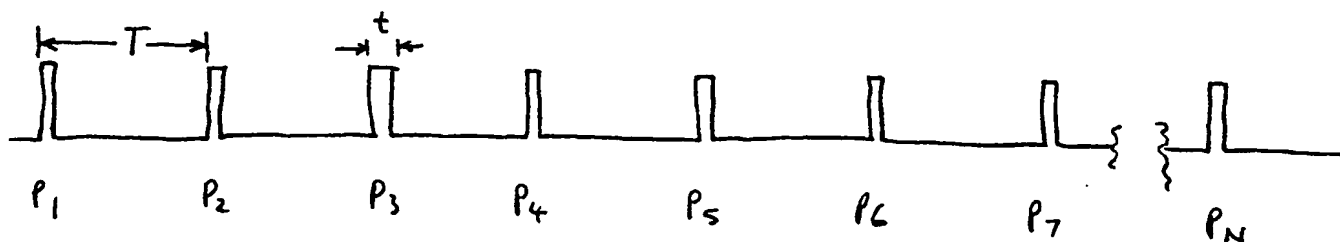
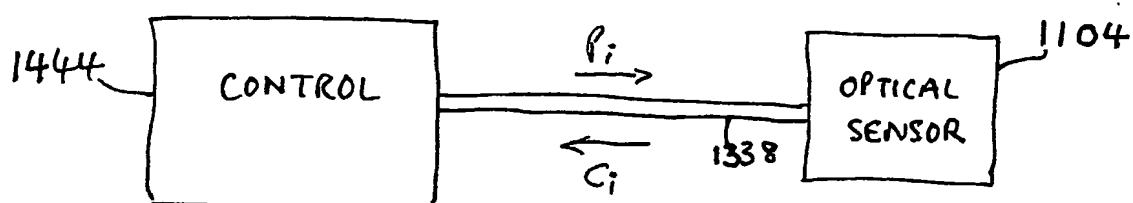


Fig.15

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(a)



(b)

$p_1$	$p_2$	$p_3$	$p_4$	$p_5$	$p_6$	$p_7$	...	$p_N$
$c_1$	$c_2$	$c_3$	$c_4$	$c_5$	$c_6$	$c_7$	...	$c_N$
	$(c_2 - c_1)R$	$(c_3 - c_2)R$	$(c_4 - c_3)R$	$(c_5 - c_4)R$	$(c_6 - c_5)R$	$(c_7 - c_6)R$	...	$(c_N - c_{N-1})R$

(c)

Fig.17

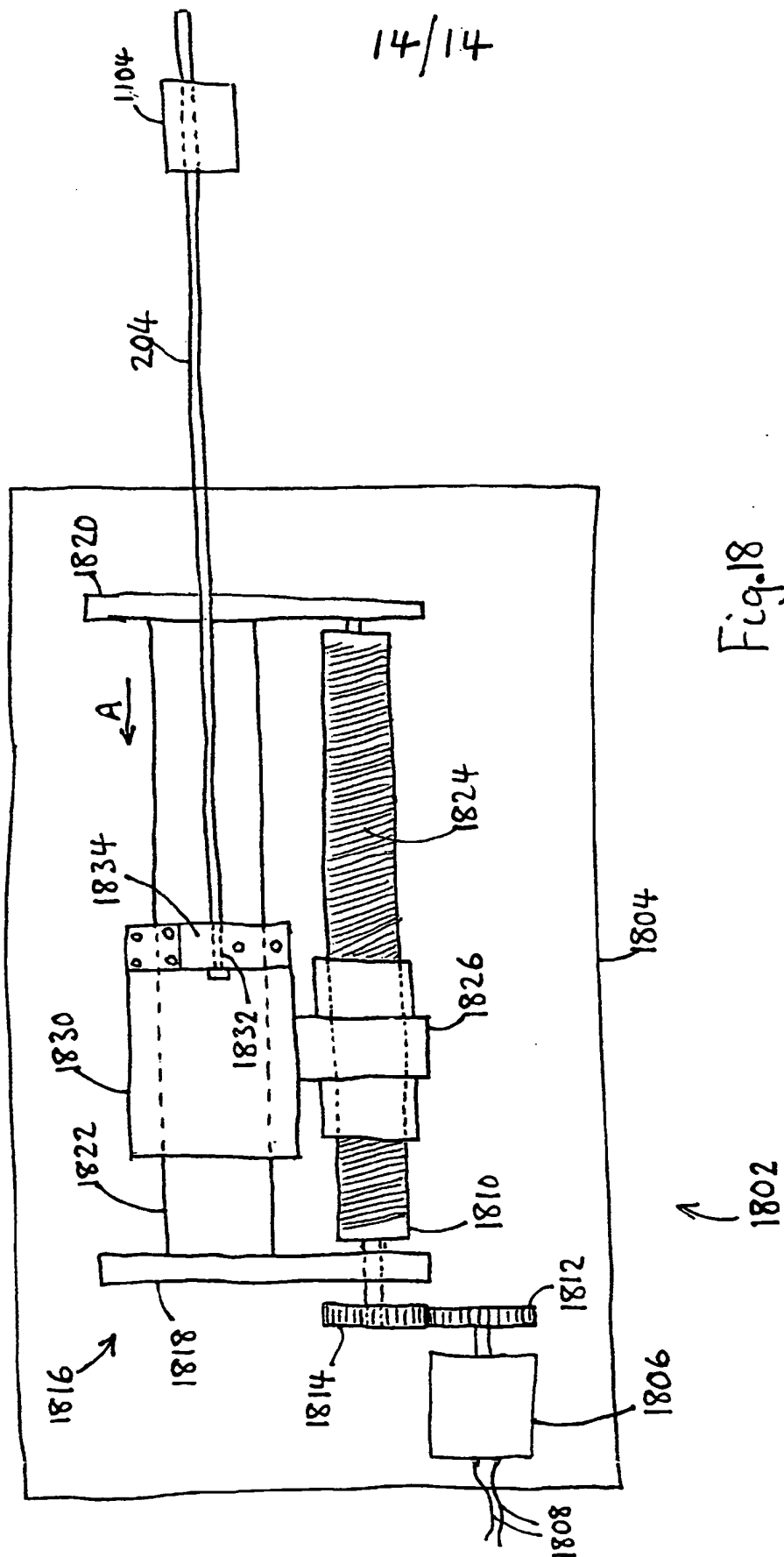


Fig. 18

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